

- **Verification- and Extraction Solutions for 3D Stacks – 30'**
Dusan Petranovic from **Mentor** Graphics
- **A Holistic Methodology for 3D/2.5D Realization – 30'**
Brandon Wang from **Cadence** Design Systems
- **Hybrid Memory Cube – 30'**
Mike Black from **Micron** Technology
- **3D Panel: Are we there yet ? – 30'**
Ivo Bolsens, Xilinx
Gary Smith, GarySmithEDA
Herb Reiter, eda2asic
Dusan Petranovic, Mentor
Brandon Wang, Cadence
Mike Black, Micron
Gene Jacobowski, E-System Design

**Gordon Moore's Law:
2x every 24 months**



<http://www.intel.com/technology/mooreslaw/>

**~ 50 years of Shrinking
Bulk-CMOS Planar Transistors**

FD-SOI

FinFETs

2.5/3D -ICs

