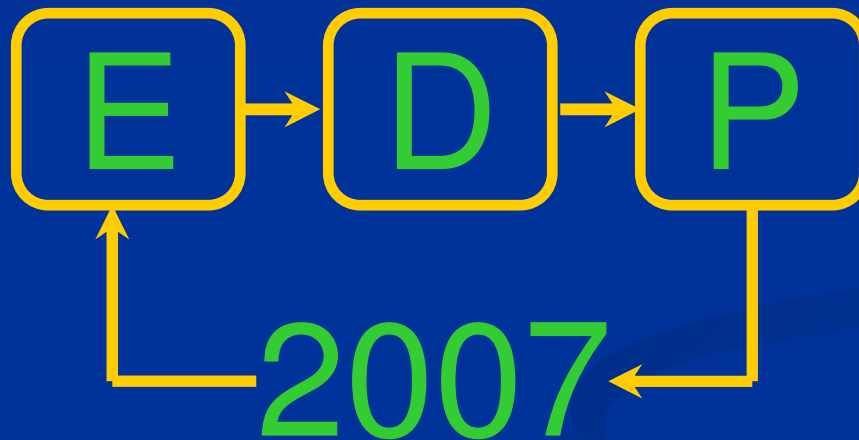
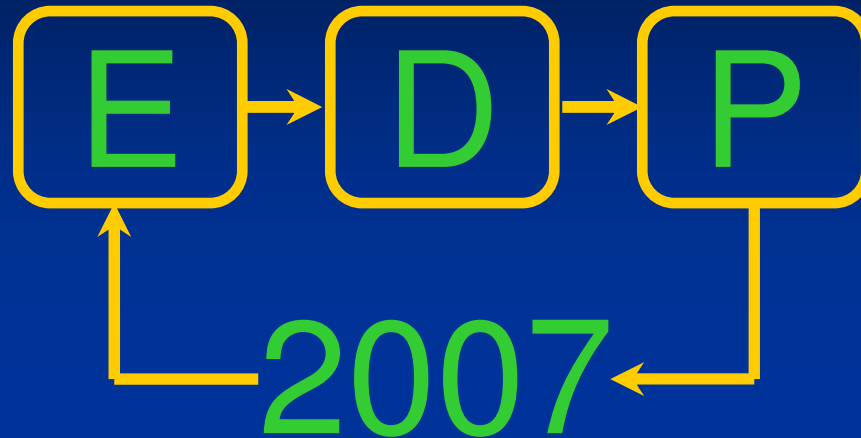


The Fourteenth IEEE/DATC Electronic Design Processes Workshop



Monterey Beach Resort
Monterey, CA : April 12-13, 2007



Welcome to IEEE/DATC EDP 2007!

Bhanu Kapoor

Monterey, CA : April 12, 2007

IEEE/DATC EDP 2007

EDP 2007

- EDP 2007 organized around the themes of Power and DFM Methodologies
 - *Power Focus (11 Presentations)*
 - *DFM Sessions (7)*
 - *EDA Standards (4)*
 - *Multi-Core Sessions (5)*
- Total of 31 Outstanding Talks

EDP 2007 Keynotes

■ Thursday Morning Keynote

■ **Leon Stok, IBM**

■ *Design Rules: From Restrictions to Prescriptions*



■ Friday Morning Keynote

■ **Andrew Singer, Rapport Inc.**

■ *So Many Cores, So Little Time*



■ Thursday Night Dinner Banquet Talk

■ **Grant Martin, Tensilica**

■ *Confluence of ESL and Configurable Multi-Core Processors*



Participating Organizations

- Freescale
- Intel
- IBM
- NEC
- NXP Semiconductor
- Qualcomm
- Rapport
- Texas Instruments
- Velogix
- ArchPro
- ARM
- Atrenta
- Cadence
- Imperas
- Magma
- Mentor Graphics
- Mercury Systems
- Sequence Design
- Si2
- Synopsys
- Tela Innovations
- Tensilica
- Purdue University
- SUNY
- UC SD
- University of Michigan
- Virginia Tech.
- GarySmithEDA
- Argon Ventures
- EE Times/CMP

EDP Organizing Committee

■ Program Chairs

- *Patrick Madden, SUNY*
- *Dennis Sylvester, University of Michigan*

■ Publications Chair

- *Kumar Venkatramani, SoftJin*

■ Publicity/Web Chair

- *Steve Grout, Consultant*

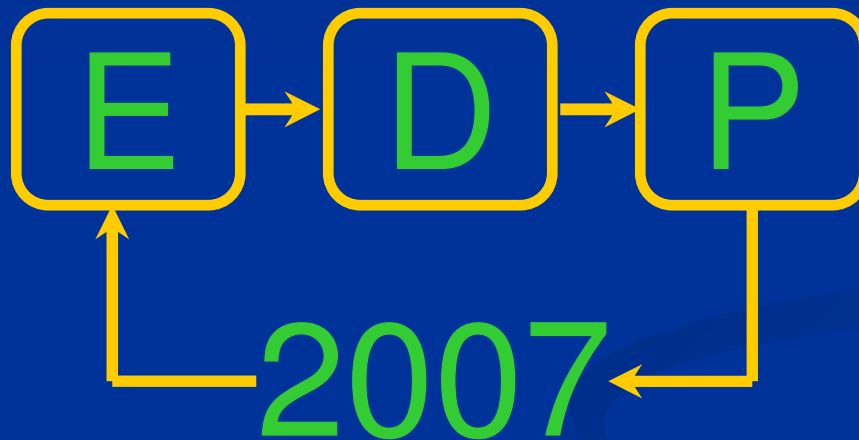
■ Special Thanks

- *Gary Smith, GarySmithEDA*
- *Dwight Hill, Synopsys*
- *Juan-Antonio Carballo, Argon Ventures*
- *John Lillis, UIC, Past Chair*

Welcome to EDP 2007

- Thanks to all speakers
 - Coming here despite severe travel restrictions
- Outstanding series of talks, like every year
- Welcome to the EDP Family
- EDP looks forward to your continued involvement and participation

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