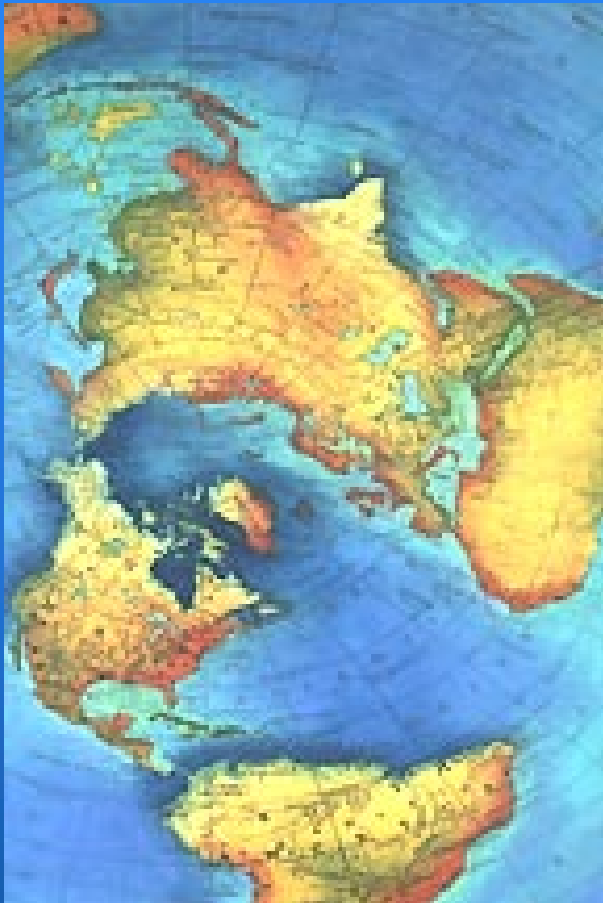


*The Electronic Design
Challenge
(DATC -EDP 2000)*



Gary Smith

Chief Analyst

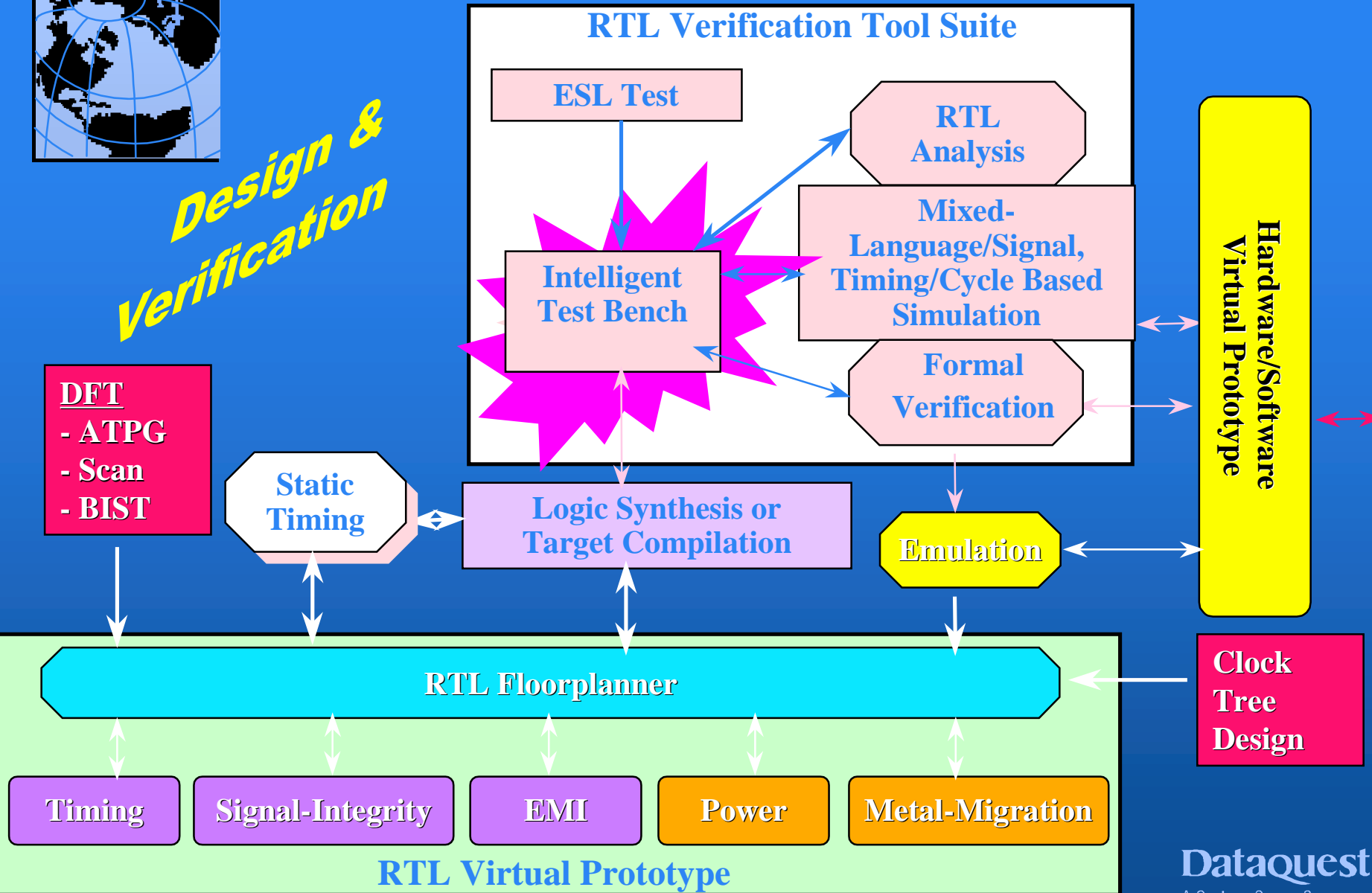
EDA, Worldwide

Dataquest

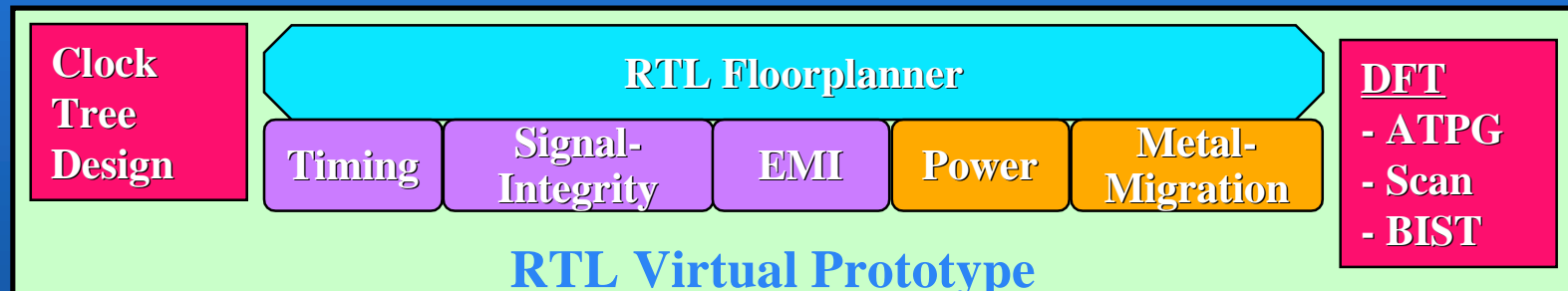
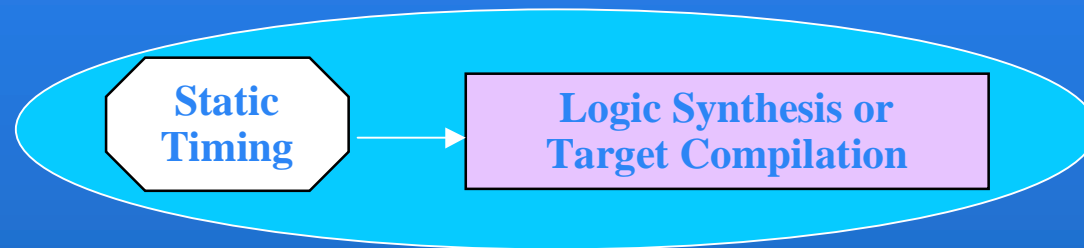
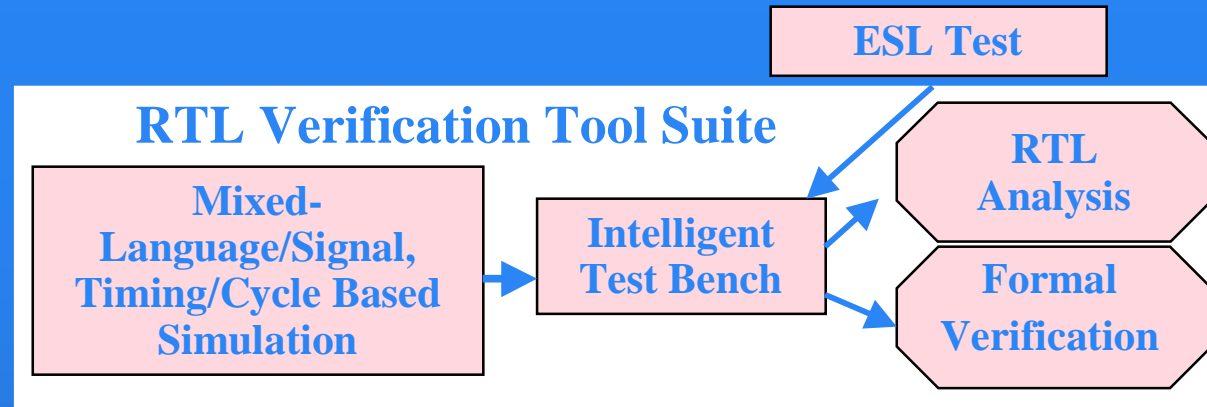
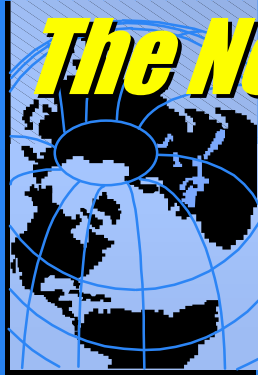
The New RTL Methodology 1999



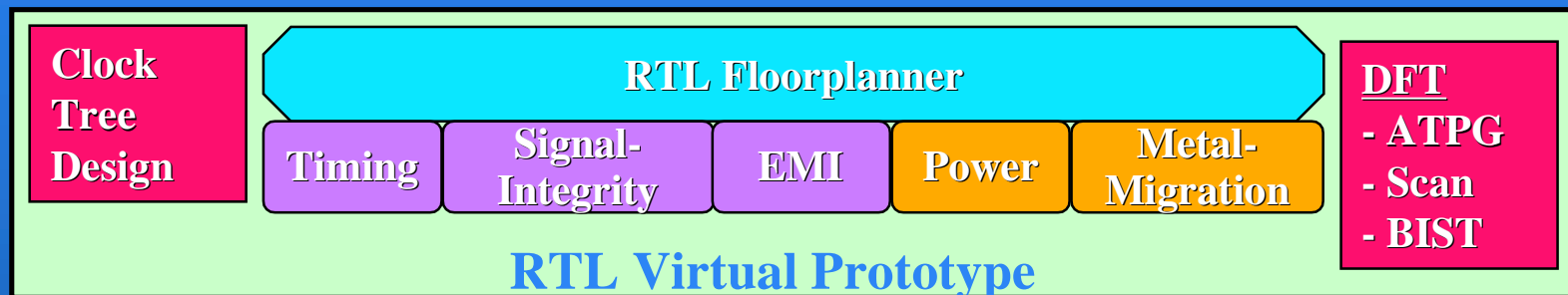
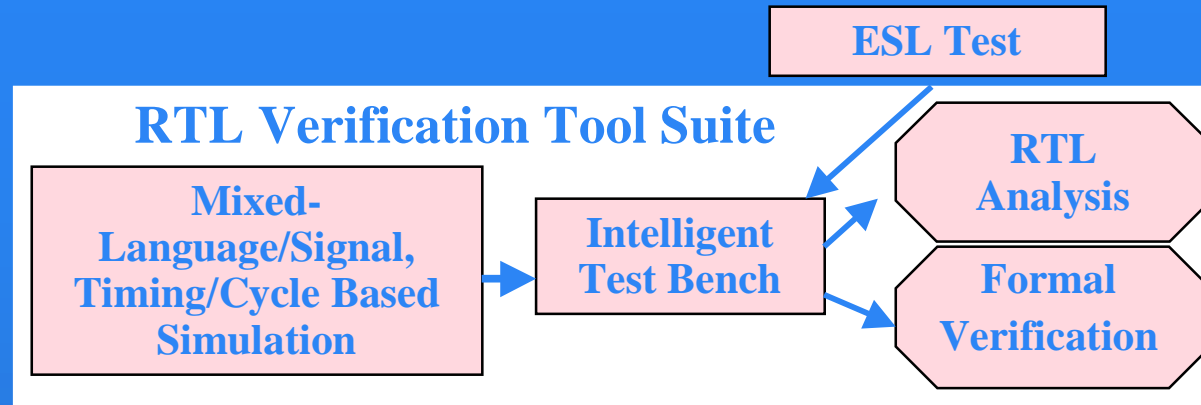
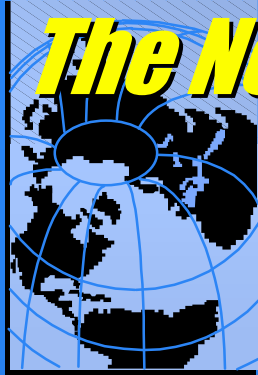
Design & Verification



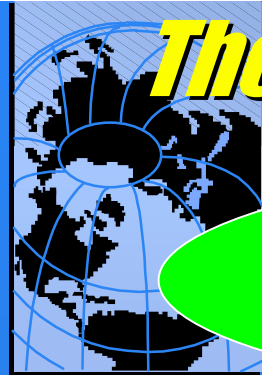
The New Implementation Methodology 2000



The New Implementation Methodology 2002



IC Implementation
(IC CAD + Synthesis & Timing)



The New Design Methodology 2004 ?

**Core/Communications
Based Design**
(ES Level)

RTL Virtual Prototype

RTL Design Planner

IC Implementation
(IC CAD + Synthesis & Timing + RTL Verification)